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## APPLICANTS

Dalson Ye Seng Kim, Singapore, SINGAPORE;

 Jeffrey Toh Tuck Fook, Singapore, SINGAPORE;  
 Lee Choon Kuan, Singapore, SINGAPORE;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/633,434 08/01/2003 PAT 6,903,449

KR

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

KR none

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/27/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY SINGAPORE	SHEETS DRAWING 6	TOTAL CLAIMS 27	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>	INITIALS <i>KR</i>		
Verified and Acknowledged				

## ADDRESS

 22823  
 STEPHEN A GRATTON  
 THE LAW OFFICE OF STEVE GRATTON  
 2764 SOUTH BRAUN WAY  
 LAKEWOOD, CO  
 80228

## TITLE

Method for fabricating semiconductor component with chip on board leadframe

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
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